

REMARKS

Reconsideration of the rejection of claim 5 is respectfully requested. It calls for each of the processors to have their own instruction sets.

Material is cited within the cited reference that suggests that the reference can operate in an SIMD mode or an MIMD mode. However, neither mode provides any intelligence about whether or not each of the processors has its own instruction set. A common instruction set can be used by all the processors and one can still be in the SIMD mode or equally well in the MIMD mode.


MIMD mode is a computer that can process two or more independent sets of instructions simultaneously on two or more sets of data. Computers with single CPUs or dual cores may use MIMD architecture. SIMD is a computer that performs one operation on multiple sets of data. It is typically used to add or multiply eight or more sets of numbers at the same time. Hardware registers are loaded with the numbers and the mathematical operation is performed on all registers simultaneously. The SIMD capability was added to Pentium CPUs, starting with the Pentium MMX chip and in subsequent Pentiums. See pertinent material from Free On Line Dictionary.

Thus, there is no reason to divine from either SIMD operations or MIMD operations that groups of parallel processors have different instruction sets.

Therefore, reconsideration of the rejection of claim 5 is respectfully requested.

Respectfully submitted,

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Timothy M. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
1616 South Voss Road, Suite 750
Houston, TX 77057-2631
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation

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(Multiple Instruction stream Multiple Data stream) A computer that can process two or more independent sets of instructions simultaneously on two or more sets of data. Computers with multiple CPUs or single CPUs with dual cores are examples of MIMD architecture. Hyperthreading also results in a certain degree of MIMD performance as well. Contrast with SIMD. See hyperthreading and dual core.

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(Single Instruction stream Multiple Data stream) A computer that performs one operation on multiple sets of data. It is typically used to add or multiply eight or more sets of numbers at the same time for multimedia encoding and rendering as well as scientific applications. Hardware registers are loaded with numbers, and the mathematical operation is performed on all registers simultaneously.

SIMD capability was added to the Pentium CPU starting with the Pentium MMX chip and enhanced in subsequent Pentiums (see MMX and SSE). Array processors are machines specialized for SIMD operations (see array processor).

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